

The Semiconductor Integrated Circuits Layout-Design Act, 2000

Finin2min provision-wise implementation overview

Authority: Ministry of Electronics and Information Technology / SICLDR. Source gateway: <https://www.indiacode.nic.in/handle/123456789/1362/simple-search?query=Semiconductor+Integrated+Circuits+Layout-Design+Act%2C+2000>

Act in 2 Minutes

This repository contains 96 numbered section identifiers and 5 subordinate-instrument articles. Use it to classify the legal issue, identify the provision and instrument, establish the event date, document authority and evidence, compute any amount or deadline, and select the correct remedy.

Editorial provision map

Sections	Topic
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11-25	Registrability And Application
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Core subordinate instruments

- Semiconductor Integrated Circuits Layout-Design Rules, 2001
- Registry forms and fee schedule
- Application, opposition and rectification procedure
- International reciprocity notifications
- Appellate and High Court procedure

Page standard

Each section page includes a Finin2min summary, controlled source map, paragraph-wise reading framework, instrument bridge, example, authority and evidence controls, financial/remedy analysis, limitation/forum control, cross-law overlays, practical Q&A; and a decision flow.

Source boundary

The official signed text, amendment and commencement record prevails. State laws, court rules, fees, thresholds, regulator directions and event-date judgments require separate current verification.